Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination | GASPARIK, FRANTISEK | Examiner | Art Unit | Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-2002/0018526 A1	02-2002	Osaka et al.	375/257
	В	US-6,742,167	05-2004	Grau, Gunter	716/4
	С	US-6,289,490	09-2001	Boyd et al.	716/2
Г	D	US-			
	Ε	US-			
	F	US-			
	G	US-			
	н	US-			
	ı	US-			
	J	US-			
	К	US-			
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FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N .					
	0					
	Р			,		
	α				*	
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tallinger et al., "Tools for on-chip interconnect inductance extraction", August 2002, pp. 1-6
	٧	Sercu et al., "EXPERIMENTAL CIRCUIT MODEL GENERATION OF NON-UNIFORM COUPLED MULTI-CONDUCTOR STRUCTURES," IEEE, 1997, PP. 1781-1784.
	w	Beker, "Numral and Experimental Modeling of High-Speed Cables and Interconnets," IEEE, 1997, pp. 898-904.
	x	Larry Smith, "Simultaneous Switch Noise and Power Plane Bounce for CMOS Technology," IEEE, 1999, pp. 163-166.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination | GASPARIK, FRANTISEK | Examiner | Art Unit | Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	А	US-			
	В	US-			
	С	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
	ı	US-			
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	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q	·			·	
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	Beyene et al., "MODELING AND ANALYSIS OF POWER DISTRIBUTION NETWORKS FOR GIBABIT APPLICATIONS," IEEE, 8/2003, PP. 1-6.				
	٧	Karkashdze et al., "A FAST METHOD OF AUXILIARY SOURCE BASED CALCULATION OF THE CAPACITANCE AND INDUCTANCE MATRICES," IEEE, 2001, PP. 187-190.				
	W	TDA, TDA Systems, Inc., "High-Density Interconnect (HDI) Magazine, March 2001, pp. 1-16."				
	х	Young et al., "Measurement of Package Inductance and Capacitance Matrices," IEEE, Feb. 1996, pp. 225-229.				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.